



ANFIS Based Control Scheme for H-Bridge Multilevel PV Inverter with Individual MPPT Control in 3-Phase Grid-Applications

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ABSTRACT

This paper deals with design and simulation of ANFIS based control scheme for H-Bridge multilevel PV inverter with individual MPPT control in 3-phase grid applications. This type of inverter topology helps to increase the flexibility and efficiency of PV systems. For better operation of PV modules and increase the solar power extraction, an individual MPPT control scheme is applied to 3-phase multilevel inverters at each phase, it will allows independent control of each dc-link voltage. as PV panel mismatches, it produce an unbalanced delivered power, which leads to unbalanced grid current in 3-phase gridconnected applications. In order to solve this problem, a ANFIS based control scheme with modulation compensation is proposed. Simulation results in terms of THD are presented to verify the possibility of the proposed approach by using MATLAB SIMULINK model design.

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1. Introduction

Solar energy is an alternate energy resource or an energy-resource complementary in hybrid systems has a scope of possibility due to the increase of research and development work in this area. In order to make best use of the PV systems, a reasonable cost, a high reliability, and a user-friendly design must be achieved in the proposed PV topologies. Several standards given by the utility companies must be obeyed in the PV-module connection. Nowadays, the standards EN61000-3-2 [1], IEEE1547 [2], and the U.S. National Electrical Code (NEC) 690 [3] are being considered. These standards deal with issues like power quality, detection of islanding operation, grounding, etc.

They define the structure and the features of the present and future PV modules. In this paper cascaded H-Bridge multilevel inverter are proposed it is shown in Fig1.Cascaded inverters involves several converters connected in series, thus, the high power and high voltage extract from the combination of the multiple modules. This topology would favor in large and medium grid-connected PV systems [12]-[14]. Each PV module has its own dc/ac converter, and the modules with their connected converters are connected in series to generate a high dc voltage, which is provided to a simplified dc/ac electrical converter. This system is combined topologies of series inverters and ac-module inverters and offers the benefits of individual maximum power point (MPP) tracking (MPPT), but the main advantage of this method is less costly and more efficient than ac-module inverters.

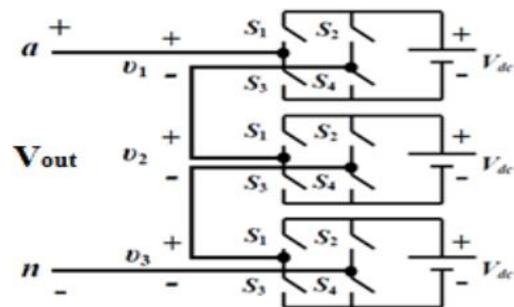


Fig 1.Cascade H-Bridge Multilevel Inverter

However, there are two power conversion stages in this configuration. Here each PV panel is connected to each dc/ac inverter and it creates one module, those modules are placed in series to generate a high-voltage level [17]-[20]. The benefits of this cascaded inverter are “one converter per panel,” for better utilization per PV module. In addition, this dc/ac cascaded inverter eliminates the necessity of per-string dc bus and the central dc/ac inverter, which further increases the overall efficiency. In this topology multilevel inverter requires an isolated dc source for each H-bridge inverter. The separate DC links are used in the multilevel inverter, it creates an individual voltage control. As a result, individual MPPT control in each PV module can be achieved, and it extracts maximum quantity of energy from PV panels. Meanwhile, the modularity and low cost of multilevel electrical converters locate them as a major issue for the

future generation for efficient and reliable grid connected solar power electronics.

The panel mismatch problems are indicated to show the necessity of individual MPPT control. In 3- ϕ grid-connected PV systems, if each PV module is operated at its individual MPP, panel mismatches may cause unbalanced power delivered to the multilevel inverter, which leads to unbalanced injected grid current. In order to get the balance 3-phase grid current, a modulation compensator is added in the control scheme. A 3-phase modular cascaded multilevel inverter topology has been assembled. Each inverter is connected to one solar panel. This modular design will increase the efficiency and flexibility of the system and it is very economical. The H-bridge inverter is connected to the grid through L filters, for reduction of switching harmonics in the current. Simulation results are provided to establish the developed control scheme.

2. System Description

Topology of the modular cascaded H-bridge multilevel inverter for three-phase grid-connected PV systems shown in Fig-2. 'n' H-bridge inverters are connected in series per phase, and dc link of each H-bridge can be fed by a solar panel. This modular design is connected to the grid through L filters, it is used to decrease the switching harmonics in the system. A variety combination of four switches for each H-bridge module, it generates three output voltage levels. $-v_{dc}$, 0, or $+v_{dc}$. The cascaded multilevel inverter with n input sources will offer $2n+1$ levels to produce the ac output waveform. This $(2n+1)$ -level voltage waveform enables the reduction of harmonics in the produced current, and also reducing the size of the required output filters.

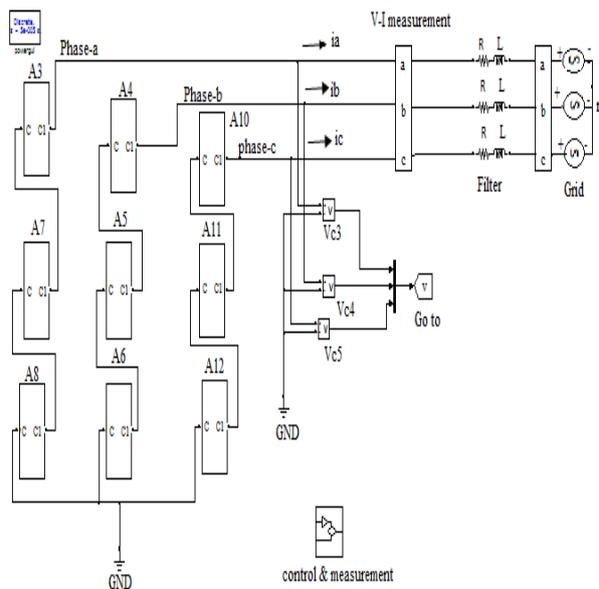


Fig 2. The modular cascaded H-bridge multilevel PV inverter for 3-phase grid applications

Table 1 System Parameters

Parameters	Values
DC link capacitor	3600 μ F
Connection Inductor	2.5mH
Grid Resistor	0.1 Ω
Grid rated Phase voltage	60 Vrms
Switching Frequency	1.5kHz

3. Panel Mismatches

Let us discuss about an operating condition that each panel has a different irradiation from the sun panel 1 has irradiance $S=1000W/m^2$, and panel 2 has $S= 600W/m^2$. If only panel 1 is tracked and its MPPT controller defines the average voltage of two panels, power extracted from panel 1 would be 133W, and the power from panel 2 would be 70W. Without individual MPPT control, the total power collected from the PV panels is 203W.

The maximum output power values of two panels will be 185W and 108.5W, when the S values are $1000W/m^2$ and $600 W/m^2$, respectively. Which means that the total power extract from the PV panels would be 293.5W if individual MPPT can be achieved. This higher value is about 1.45 times of the earlier one. PV mismatch is an essential issue due to the different irradiance, unequal temperatures, and aging of the PV panels. MPP of each PV module may be different for each H-bridge is equipped with its own PV panel.

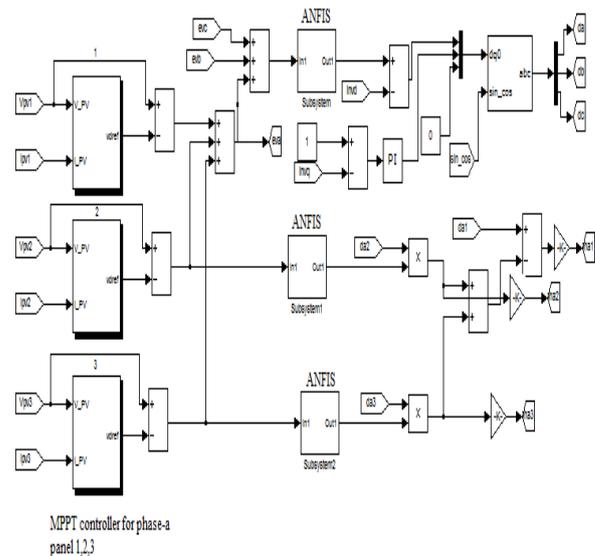


Fig 3. ANFIS based control scheme for 3-phase modular cascaded H-bridge multilevel PV inverter.

Each PV module is connected to its own MPPT control, a control scheme is offered for this application which is very efficient. The Individual MPPT control of cascaded H-bridge multilevel inverter is shown in Fig. 3. In each module consists of self MPPT controller, here each PV module indicates the combination of PV panel, DC-link and H-Bridge inverter is connected to produce the DC-link

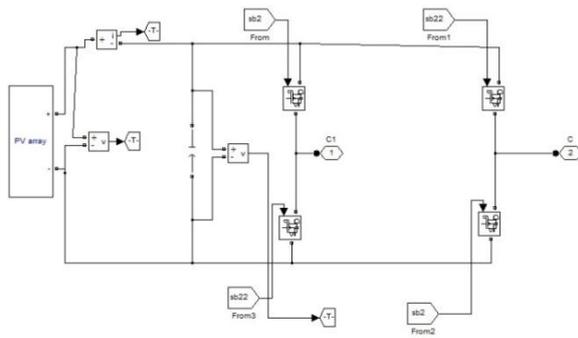


Fig 4 PV-Array Controllers

In this, output of MPPT block is given to ANFIS controller, which is used to diminish the error between desired value and measured value. For each module, an MPPT controller is connected to produce the dc-link voltage reference. Each dc-link voltage is compared with the resultant voltage which is taken as reference, and the sum of all errors is controlled by ANFIS controller that gives the current reference 'I_{dref}'. The reactive current reference 'I_{qref}' can be set to zero, or if reactive power compensation is required, 'I_{qref}' given by a reactive current calculator. The synchronous reference frame phase-locked loop (SRF-PLL) is used to find the frequency and phase angle of the active current reference. As the typical control scheme in 3-phase systems, the grid current in *abc* coordinates are converted into *dq* coordinates and controlled through PI controllers, to create the modulation index in the *dq* coordinates.

In order to make each PV module operate at its self MPP, consider phase 'a' as an example; the voltages V_{dca₂} to V_{dca_n} are controlled individually through *n-1* loops. Each voltage controller gives the modulation index proportion of one H-bridge module in phase *a*. After multiplied by the modulation index of phase *a*, *n-1* modulation indices can be found.

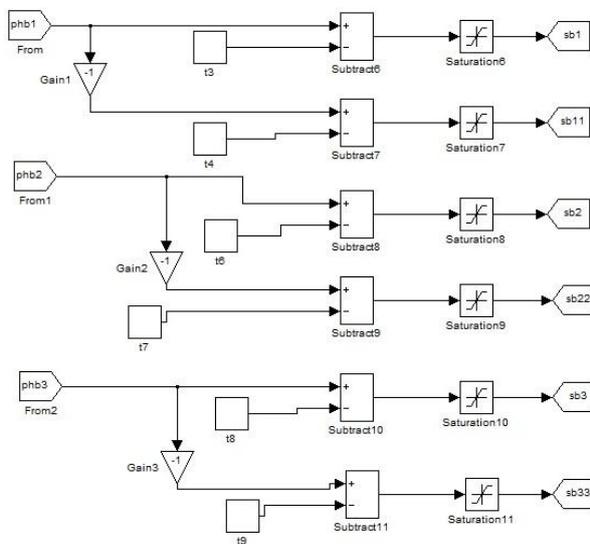


Fig 5 Phase shift pulse circuit for each phase.

The control schemes in phase 'b' and 'c' are almost the same. The only difference is that all dc-link voltages are controlled through ANFIS logic controllers, and *n* modulation index

proportions are obtained for each phase. A phase-shifted sinusoidal pulse width modulation switching scheme is applied to control the switching devices of each H-bridge it will shown in Fig: 5. It can be observed that there is one H-bridge module out of *N* modules whose modulation index is obtained by subtraction. For three-phase system, *N=3n*, where *n* is the number of H-bridge modules per phase.

The reason is that *N* voltage loops are necessary to manage different voltage levels on *N* no of H-bridges, and one is the total voltage loop, which gives the current reference. So, only *N-1* modulation indices can be determined by the last *N-1* voltage loops, and one modulation index has to be obtained by subtraction. The incremental conductance technique is used in this paper.

3.1 Incremental conductance method

Incremental conductance method are used for maximum energy extraction from PV panels, in this controller it measures incremental changes in PV array current and voltage to calculate the effect of change in voltage. This method requires more calculations in the controller, but can easily track the track changing conditions and more quickly than the perturb and observe method (P&O). Like the P&O algorithm, it can produce oscillations in power output. This method utilizes the incremental conductance (*dI/dV*) of the photovoltaic array to calculate the sign of the change in power with respect to change in voltage (*dP/dV*). The incremental conductance method calculates the maximum power point by comparison of the incremental conductance ($\Delta I / \Delta V$) to the array conductance (*I / V*). When these two are the same ($I / V = \Delta I / \Delta V$), the output voltage is the MPP voltage. The controller maintains this voltage until the irradiation changes and the process is repeated.

$$\frac{dp}{dv} = 0, \text{ at MPP}$$

$$\frac{dp}{dv} > 0, \text{ at left of MPP}$$

$$\frac{dp}{dv} < 0, \text{ at right of MPP}$$

$$\frac{dp}{dv} = \frac{d(Iv)}{dv}$$

$$= I + v \frac{dI}{dv}$$

So that, $\frac{\Delta V}{\Delta V} = -\frac{I}{V}$ at MPP

$$\frac{\Delta V}{\Delta V} > -\frac{I}{V}, \text{ at left of the MPP}$$

$$\frac{\Delta V}{\Delta V} < -\frac{I}{V}, \text{ at right of the MPP}$$

The incremental conductance method have more advantages compare to perturb and observe (P&O) method. But this method can determine the maximum power point without oscillating around this value.

3.2 Modulation Compensation Technique

In order to overcome the unbalanced grid current problem, a zero sequence voltage can be implemented in phase legs in order to affect the current flowing into each phase. If the simplified inverter phase voltage is proportional to the unbalanced power, it leads to generate balanced current.

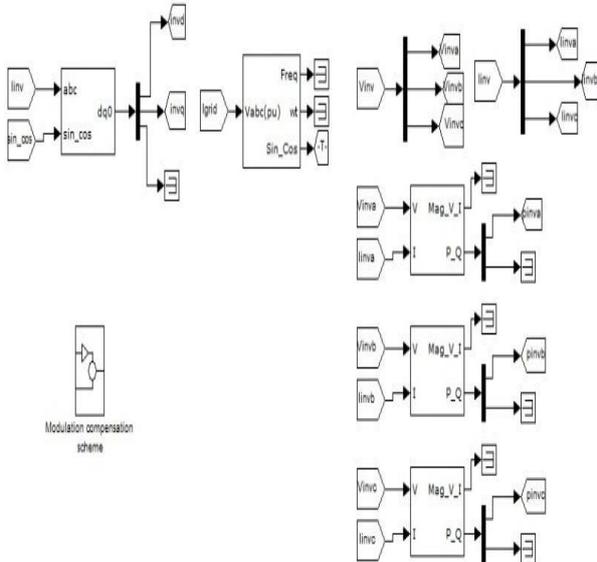


Fig 6 Modulation compensation and measurements

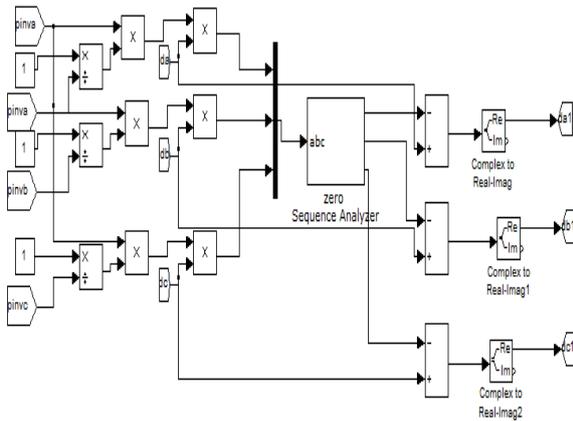


Fig 7 Sub-circuit for Modulation compensation scheme

The Fig.7 indicates modulation compensation block which is added to the control scheme of 3-phase modular cascaded H-Bridge multilevel PV inverters. The main concept is how to update the modulation index of each phase without increasing the complexity of the control system.

First, the unbalanced power is weighted by ratio r_j , which is calculated as

$$r_j = \frac{P_{inav}}{P_{inj}} \quad (1)$$

Where ' P_{inj} ' is the input power of phase ' j ' ($j=a, b, c$), and ' P_{inav} ' is the average input power. Then, the injected zero sequence modulation index can be generated as

$$d_o = \frac{1}{2} [\min(r_a, d_a, d_b, r_c, d_c) + \max(r_a, d_a, r_b, d_b, r_c, d_c)] \quad (2)$$

Where ' d_j ' is the modulation index of phase ' j ' ($j=a, b, c$) and is determined by the current loop controller.

The modulation index of each phase is updated by
$$d'_j = d_j - d_o \quad (3)$$

Only simple calculations are used in the scheme, which will not increase the complexity of the control system.

3.3 Adaptive neuro fuzzy inference system

In FLC, basic control action is determined by a set of linguistic rules. These rules are determined by the system. Since the numerical variables are converted into linguistic variables, mathematical modeling of the system is not required in FC.

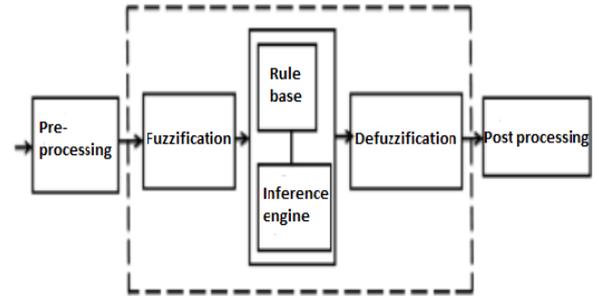


Fig 8.Fuzzy logic controller

The FLC contains three parts: Interference engine, Fuzzification, and Defuzzification. The FLC is characterized as, i. seven fuzzy sets for each input and output, ii.Triangular membership functions for simplicity, iii. Fuzzification using continuous universe of discourse, iv. Implication using mamdani 'min' operator, v. Defuzzification using the height method

Table 2 Fuzzy Rules

Change in error	Error						
	NB	NM	NS	Z	PS	PM	PB
NB	NB	NB	NB	NB	NM	NS	Z
NM	NB	NM	NM	NM	NS	Z	PS
NS	NB	NM	NS	NS	Z	PS	PM
Z	NB	NM	NS	Z	PS	PM	PB
PS	NM	NS	Z	PS	PM	PM	PB
PM	NS	Z	PS	PM	PM	PM	PB
PB	Z	PS	PM	PM	PB	PB	PB

Fuzzification: Membership function values are assigned to the linguistic variables, using seven fuzzy subsets: NB (Negative Big), NS (Negative Small), NM (Negative Medium), ZE (Zero), PS (Positive Small), PM (Positive Medium), and PB (Positive Big). The Partition of fuzzy subsets and the shape of membership CE (k) E (k) function adapt the shape up to appropriate system. The value of input error and change in error are normalized by an input scaling factor.

In this system the input scaling factor is designed such that input values are between -1 and +1. The triangular membership function of this arrangement presumes that for any particular E(k) input there is only one dominant fuzzy subset. The input error for the FLC is given as

$$E(k) = \frac{P_{ph(k)} - P_{ph(k-1)}}{V_{ph(k)} - V_{ph(k-1)}} \quad (4)$$

$$CE(k) = E(k) - E(k-1) \quad (5)$$

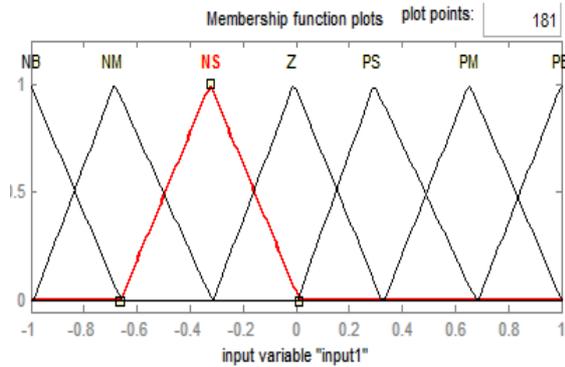


Fig 9.Membership functions

Inference Method: Several configuration methods such as Max–Min and Max-Dot have been in the literature. In this paper Min method is used. The output membership function of each rule is given by the minimum operator and maximum operator. Table 1 shows rule base of the FLC.

Defuzzification: As a plant usually requires a non-fuzzy value of control, a defuzzification stage is needed. To compute the output of the FLC, „height“ method is used and the FLC output modifies the control output. Further, the output of FLC controls the switch in the inverter. In active power, reactive power, terminal voltage of the line and capacitor voltage are required to be maintained. In order to control these parameters, they are sensed and compared with the reference values. To achieve this, the membership functions of FLC are: error, change in error and output .The set of FLC rules are derived from

$$u = -[\alpha E + (1-\alpha)C] \quad (6)$$

Where ‘ α ’ is self-adjustable factor which can regulate the entire operation. ‘E’ is the error of the system, C is the change in error and u is the control variable. A huge value of error ‘E’ indicates the given system is in unbalanced state. When the system is in unbalanced state, the controller should increase its control variables to balance the system as early as possible. One the other hand, low value of the error ‘E’ indicates that the system is near to balanced state.

5. Results and Discussions

The 3-phase grid connected PV inverter is simulated in two different conditions. First, all PV panels are operated under the same irradiance $S = 1000\text{W/m}^2$ and temperature $T = 25\text{ }^\circ\text{C}$. At $t = 0.8\text{ s}$, the solar irradiance on the first and second panels of phase a decreases to 600 W/m^2 , and that for the other panels stays the same. The dc-link voltages of phase a are shown in Fig.10. At the beginning, all PV panels are operated at an MPP voltage of 36.4V.

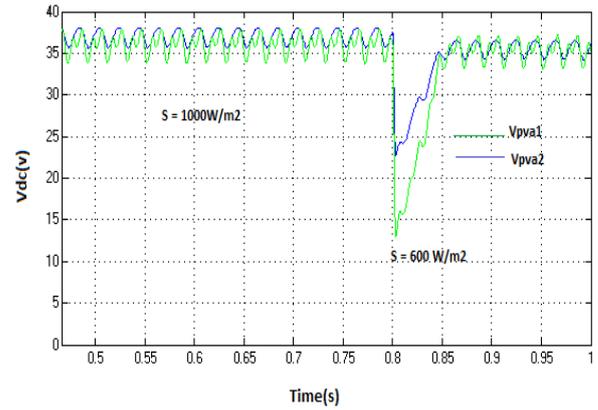


Fig.10 (a)

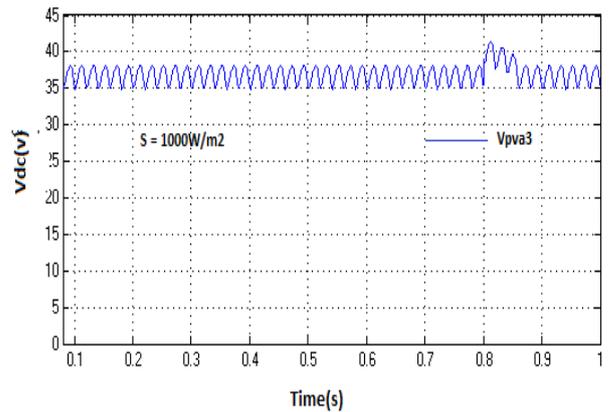


Fig.10(b)

Fig 10(a) DC-link voltages of phase ‘a’ with distributed MPPT ($T = 25\text{ }^\circ\text{C}$). (a) DC-link voltage of modules 1 and 2 (b) DC-link voltage of module 3

As the irradiance changes, the first and second dc-link voltages decrease and track the new MPP voltage of 36 V, while the third panel is still operated at 36.4 V. The PV current waveforms of phase ‘a’ are shown in Fig. 11. After $t = 0.8\text{ s}$, the currents of the first and second PV panels are much smaller due to the low irradiance, and the lower ripple of the dc-link voltage can be found in Fig. 10(a).

The dc-link voltages of phase ‘b’ are shown in Fig.10. All phase-b panels track the MPP voltage of 36.4 V, which shows that they are not influenced by other phases. With the distributed MPPT control, the dc-link voltage of each H-bridge can be controlled independently. In other words, the connected PV panel of each H-bridge can be operated at its own MPP voltage and will not be influenced by the panels connected to other H-bridges. Thus, more solar energy can be extracted, and the efficiency of the overall PV system will be increased.

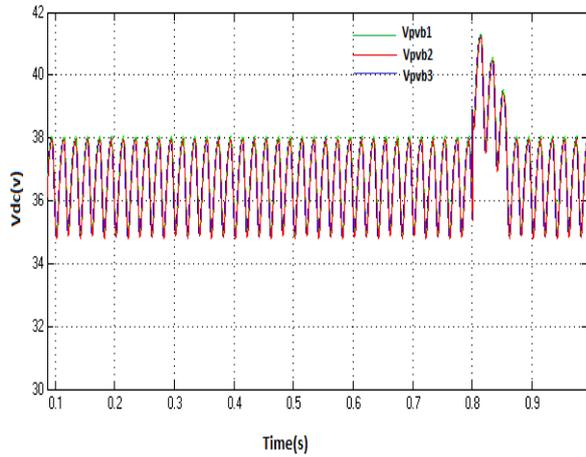


Fig 11. PV currents of phase 'a' with distributed MPPT ($T = 25^{\circ}\text{C}$)

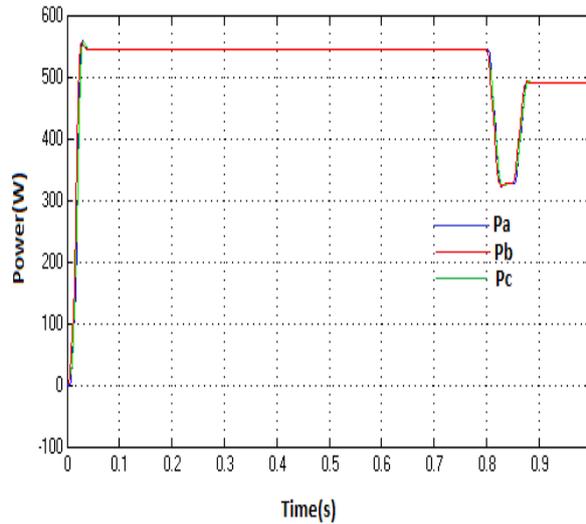


Fig 12 DC-link voltages of phase 'b' with distributed MPPT ($T=25^{\circ}\text{C}$).

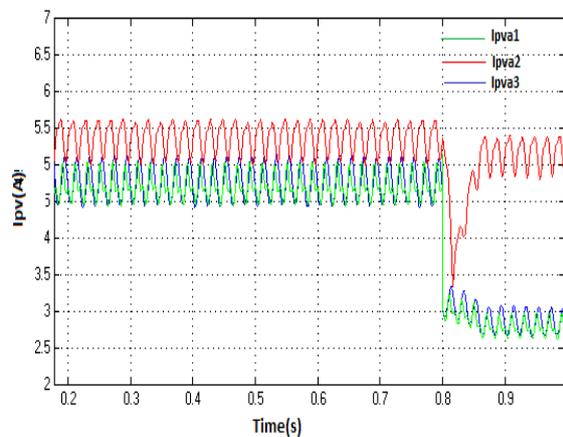


Fig 13 Power extracted from PV panels with distributed MPPT.

Fig. 13 shows the power extracted from each phase. At the beginning, all panels are operated under irradiance $S = 1000\text{W/m}^2$, and each phase is generate a maximum power of 555 W. After $t = 0.8$ s, the power harvested from phase a decreases to 400 W, and power from the other two phases remains same. Obviously, the power supplied to the three-phase grid-connected inverter is unbalanced. However, by applying the modulation compensation scheme, the power injected to the grid is still balanced, as shown in Fig. 14. In addition, by comparing the total power extracted from the PV panels with the total power injected to the grid, it can be observed that there is no extra power loss caused by the modulation compensation scheme.

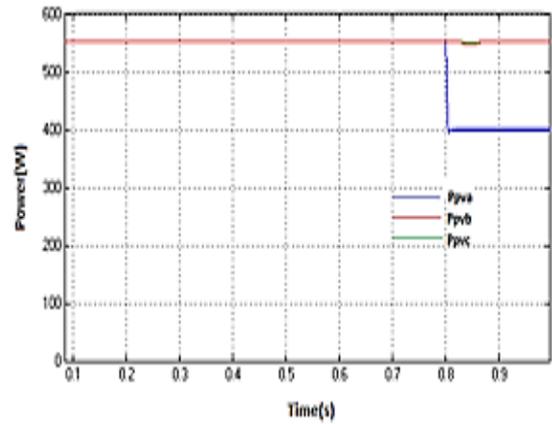


Fig 14. Power injected to the grid with modulation compensation

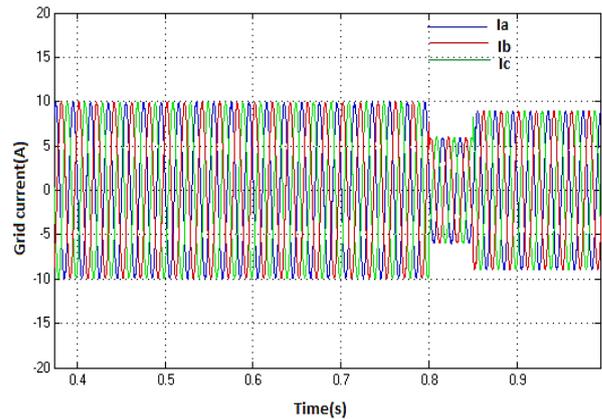


Fig 15. Three-phase inverter output voltage waveforms with modulation compensation

Fig. 15 shows the output voltages (v_{jN}) of the three-phase inverter. Due to the injected zero sequence component, they are unbalanced after $t = 0.8$ s. The THD of the grid current is 0.57%.

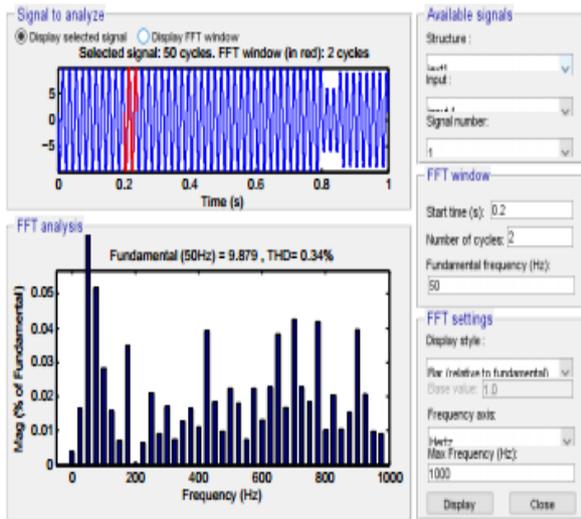


Fig 16. Three-phase grid current waveforms with modulation compensation

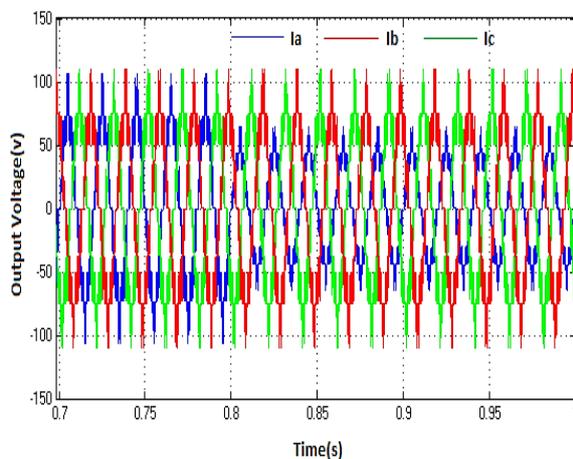


Fig 17 THD Analysis

6. Conclusion

The multilevel inverter topology will help to improve the utilization of connected PV modules if the voltages of the separate dc links are controlled independently. Thus, a distributed MPPT control scheme for both single- and three-phase PV systems is applied to increase the overall efficiency of PV systems. For the three-phase grid-connected PV system, PV mismatches may introduce unbalanced supplied power, resulting in unbalanced injected grid current. A modulation compensation scheme, which will not increase the complexity of the control system or cause extra power loss, also it is added to balance the grid current. A modular three-phase seven-level cascaded H-bridge inverter has been developed through simulation and verified with PV panels under different partial shading conditions. With the proposed control scheme, each PV module can be operated at its own MPP to maximize the solar energy extraction, ANFIS is used to reduce the THD value of grid current from 3.3% to 0.34% and the three-phase grid current is balanced even with the unbalanced supply of solar power.

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